

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

M.TECH-I SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS AUGUST-2017

T I M E T A B L E

T I M E : 2:00 PM TO 5:00 PM

NB
AR
24/7/17

COURSE	07-08-2017 MONDAY	09-08-2017 WEDNESDAY	11-08-2017 FRIDAY	17-08-2017 THURSDAY	19-08-2017 SATURDAY	22-08-2017 TUESDAY	24-08-2017 THURSDAY	26-08-2017 SATURDAY
WIRELESS AND MOBILE COMMUNICATION~65	IMAGE & VIDEO PROCESSING	ADVANCED DIGITAL SIGNAL PROCESSING	ADVANCED DATA COMMUNICATIONS	INTERNETWORKING	ELECTIVE I CPLD & FPGA ARCHITECTURES AND APPLICATIONS	ELECTIVE II RADAR SIGNAL PROCESSING		
					DETECTION AND ESTIMATION THEORY	SATELLITE COMMUNICATIONS		
ELECTRONICS AND COMMUNICATION ENGINEERING~70	DIGITAL SYSTEM DESIGN	VLSI TECHNOLOGY AND DESIGN	ADVANCED DATA COMMUNICATIONS		ELECTIVE II ADVANCED COMPUTER ARCHITECTURE	ELECTIVE I MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN	ADVANCED DIGITAL SIGNAL PROCESSING	
					EMBEDDED REAL TIME OPERATING SYSTEMS	DETECTION AND ESTIMATION THEORY		
EMBEDDED SYSTEMS & VLSI DESIGN~77	ELECTIVE I HARDWARE SOFTWARE CODESIGN	VLSI TECHNOLOGY & DESIGN	ELECTIVE II NETWORK SECURITY & CRYPTOGRAPHY	ALGORITHMS FOR VLSI DESIGN AUTOMATION	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN	ELECTIVE II MICRO ELECTROMECHANICAL SYSTEMS	
	DIGITAL SYSTEM DESIGN							
HIGHWAY ENGINEERING~87	HIGHWAY INFRASTRUCTURE ENGINEERING	URBAN TRANSPORTATION PLANNING	TRAFFIC ENGINEERING	PAVEMENT ANALYSIS & DESIGN	ELECTIVE I BRIDGE ENGINEERING	ELECTIVE II CONCRETE TECHNOLOGY		
					APPLIED STATISTICS PROJECT MANAGEMENT	GROUND IMPROVEMENT TECHNIQUES		
						REMOTE SENSING AND GLOBAL POSITIONING SYSTEMS		

DATE: 11-07-2017

Sd/-
CONTROLLER OF EXAMINATIONS

NOTE:

- (i). ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY
- (ii). EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES.THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

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EXAMINATION BRANCH

M.TECH-I SEMESTER -R13 REGULATION-SUPPLEMENTARY EXAMINATIONS AUGUST-2017

T I M E T A B L E

T I M E : 2:00 PM TO 5:00 PM

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24/7/17

COURSE	07-08-2017 MONDAY	09-08-2017 WEDNESDAY	11-08-2017 FRIDAY	17-08-2017 THURSDAY	19-08-2017 SATURDAY	22-08-2017 TUESDAY	24-08-2017 THURSDAY	26-08-2017 SATURDAY
TRANSPORTATION ENGINEERING-22	TRAFFIC ENGINEERING AND MANAGEMENT	TRANSPORTATION INFRASTRUCTURE DESIGN	URBAN TRANSPORTATION POLICY PLANNING FOR SUSTAINABLE DEVELOPMENT	PAVENT MATERIAL CHARACTERIZATION	ELECTIVE I/II APPLIED STATISTICS ENGINEERING OF GROUND	ELECTIVE I/II TRANSPORTATION SYSTEM MANAGEMENT REMOTE SENSING AND GPS FOR TRANSPORTATION ENGINEERING	ELECTIVE I/II ADVANCED CONCRETE TECHNOLOGY	ELECTIVE I/II PROJECT MANAGEMENT
WIRELESS AND MOBILE COMMUNICATION-65	IMAGE AND VIDEO PROCESSING	TCPORIP AND ATM NETWORKS	ADVANCED DATA COMMUNICATIONS	ADVANCED DIGITAL SIGNAL PROCESSING	ELECTIVE I RF CIRCUIT DESIGN RANDOM PROCESSES AND TIME SERIES ANALYSIS DETECTION AND ESTIMATION THEORY	ELECTIVE II SATELLITE COMMUNICATIONS SPREAD SPECTRUM COMMUNICATIONS VOICE OVER IP		
ELECTRONICS AND COMMUNICATION ENGINEERING-70	DIGITAL SYSTEM DESIGN	VLSI TECHNOLOGY AND DESIGN	ADVANCED DATA COMMUNICATIONS	ADVANCED DIGITAL SIGNAL PROCESSING	ELECTIVE I RADIO NAVIGATIONAL AIDS MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN DETECTION AND ESTIMATION THEORY	ELECTIVE II EMBEDDED REAL TIME OPERATING SYSTEMS ADVANCED COMPUTER ARCHITECTURE INTERNETWORKING		
EMBEDDED SYSTEMS & VLSI DESIGN-77	CPLD AND FPGA ARCHITECTURES AND APPLICATIONS	VLSI TECHNOLOGY AND DESIGN	CMOS ANALOG INTEGRATED CIRCUIT DESIGN	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN	ELECTIVE I SOFT COMPUTING TECHNIQUES DIGITAL SYSTEM DESIGN HARDWARE SOFTWARE CO-DESIGN	ELECTIVE II ADVANCED OPERATING SYSTEMS NETWORK SECURITY AND CRYPTOGRAPHY CMOS DIGITAL INTEGRATED CIRCUIT DESIGN		
HIGHWAY ENGINEERING-87	HIGHWAY INFRASTRUCTURE ENGINEERING	PAVEMENT MATERIAL CHARACTERIZATION	TRAFFIC ENGINEERING	URBAN TRANSPORTATION PLANNING	ELECTIVE I BRIDGE ENGINEERING PROJECT MANAGEMENT APPLIED STATISTICS	ELECTIVE II REMOTE SENSING & GLOBAL POSITIONING SYSTEMS ENGINEERING OF GROUND CONCRETE TECHNOLOGY		

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M.TECH-I SEMESTER -R15 REGULATION- SUPPLEMENTARY EXAMINATIONS AUGUST-2017

T I M E T A B L E

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24/7/15*

T I M E : 2:00 PM TO 5:00 PM

COURSE	07-08-2017 MONDAY	09-08-2017 WEDNESDAY	11-08-2017 FRIDAY	17-08-2017 THURSDAY	19-08-2017 SATURDAY	22-08-2017 TUESDAY	24-08-2017 THURSDAY	26-08-2017 SATURDAY
ELECTRONICS AND COMMUNICATION ENGINEERING-70	DIGITAL SYSTEM DESIGN	VLSI TECHNOLOGY AND DESIGN	ADVANCED DATA COMMUNICATIONS	ELECTIVE-C2	ELECTIVE-C1	OPEN ELECTIVE	ELECTIVE-C1 DETECTION AND ESTIMATION THEORY	ELECTIVE-C2 REAL TIME OPERATING SYSTEMS
				<u>INTERNETWORKING</u>	EMBEDDED SYSTEM DESIGN	VIRTUAL INSTRUMENTATION		
				ADVANCED COMPUTER ARCHITECTURE	ADVANCED DIGITAL SIGNAL PROCESSING	ADVANCED COMMUNICATIONS AND NETWORKS		
						SPREAD SPECTRUM COMMUNICATION		
HIGHWAY ENGINEERING-87	URBAN TRANSPORTATION PLANNING	TRAFFIC ENGINEERING	PAVEMENT MATERIAL CHARACTERIZATION	ELECTIVE-C1	ELECTIVE-C2	OPEN ELECTIVE	ELECTIVE-C1 ROAD SAFETY ENGINEERING	
				ENGINEERING OF GROUND	HIGHWAY INFRASTRUCTURE DESIGN	REMOTE SENSING AND GIS		
				BRIDGE ENGINEERING	ADVANCED CONCRETE TECHNOLOGY	PROJECT MANAGEMENT		
					TRANSPORTATION SYSTEM MANAGEMENT	APPLIED STATISTICS		

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M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS AUGUST-2017

T I M E T A B L E

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WIRELESS AND MOBILE COMMUNICATION-65	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	TELECOMMUNICATION SWITCHING & NETWORKS	ELECTIVE III MOBILE COMPUTING TECHNOLOGIES ADHOC WIRELESS & SENSOR NETWORKS OPTICAL NETWORKS	ELECTIVE IV SOFTWARE RADIO PROPAGATION MODELS FOR WIRELESS COMMUNICATIONS NETWORK SECURITY & CRYPTOGRAPHY		
ELECTRONICS AND COMMUNICATION ENGINEERING-70	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	IMAGE & VIDEO PROCESSING	ELECTIVE III LOW POWER VLSI DESIGN RADAR SIGNAL PROCESSING OPTICAL NETWORKS	ELECTIVE IV DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES DESIGN FOR TESTABILITY NETWORK SECURITY & CRYPTOGRAPHY		
EMBEDDED SYSTEMS & VLSI DESIGN-77	CMOS ANALOG & MIXED SIGNAL DESIGN	SYSTEM ON CHIP ARCHITECTURE	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN OF FAULT TOLERANT SYSTEMS	ELECTIVE IV SYSTEM MODELING AND SIMULATION LOW POWER VLSI DESIGN SEMICONDUCTOR MEMORY DESIGN & TESTING	ELECTIVE III DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES IMAGE & VIDEO PROCESSING VLSI SIGNAL PROCESSING		
HIGHWAY ENGINEERING-87	HIGHWAY PROJECT FORMULATION & ECONOMICS	PAVEMENT CONSTRUCTION MAINTENANCE & MANAGEMENT	TRANSPORTATION SYSTEM MANAGEMENT	TRAFFIC ANALYSIS	ELECTIVE III ENVIRONMENTAL IMPACT ASSESSMENT FOR TRANSPORTATION PROJECTS ADVANCED MODELING TECHNIQUES IN HIGHWAY ENGINEERING LAND USE AND TRANSPORTATION MODELING	ELECTIVE IV ADVANCED TRAVEL DEMAND MODELLING GIS APPLICATIONS IN TRANSPORTATION ENGINEERING OPTIMIZATION TECHNIQUES		

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TRANSPORTATION ENGINEERING-22	TRAFFIC ANALYSIS	LAND USE TRANSPORTATION MODELING	HIGHWAY PROJECT FORMULATION & ECONOMICS	PAVEMENT ANALYSIS AND DESIGN	ELECTIVE III/IV INTELLIGENT TRANSPORTATION SYSTEMS	ELECTIVE III/IV RURAL ROAD	ELECTIVE III/IV PAVEMENT CONSTRUCTION MAINTENANCE AND MANAGEMENT	ELECTIVE III/IV GIS APPLICATIONS IN TRANSPORTATION ENGINEERING ENVIRONMENTAL IMPACT ASSESSMENT FOR TRANSPORTATION PROJECTS
WIRELESS AND MOBILE COMMUNICATION-65	WIRELESS COMMUNICATIONS & NETWORKS	CODING THEORY AND TECHNIQUES	OPTICAL COMMUNICATIONS TECHNOLOGY	TELECOMMUNICATION SWITCHING SYSTEMS AND NETWORKS WIRELESS LANS AND PANS	ELECTIVE III MOBILE COMPUTING TECHNOLOGIES OPTICAL NETWORKS AD-HOC AND WIRELESS SENSOR NETWORKS	ELECTIVE IV PROPAGATION MODELS FOR WIRELESS COMMUNICATIONS SOFTWARE RADIO NETWORK SECURITY AND CRYPTOGRAPHY		
ELECTRONICS AND COMMUNICATION ENGINEERING-70	IMAGE AND VIDEO PROCESSING	CODING THEORY AND TECHNIQUES	OPTICAL COMMUNICATIONS TECHNOLOGY	WIRELESS COMMUNICATIONS AND NETWORKS	ELECTIVE III OPTICAL NETWORKS RADAR SIGNAL PROCESSING SPEECH PROCESSING	ELECTIVE IV SATELLITE COMMUNICATIONS DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES NETWORK SECURITY AND CRYPTOGRAPHY		
EMBEDDED SYSTEMS & VLSI DESIGN-77	EMBEDDED C	CMOS MIXED SIGNAL CIRCUIT DESIGN	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN FOR TESTABILITY	ELECTIVE III DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES EMBEDDED NETWORKING SYSTEM ON CHIP ARCHITECTURE	ELECTIVE IV SEMICONDUCTOR MEMORY DESIGN AND TESTING LOW POWER VLSI DESIGN SENSORS AND ACTUATORS		
HIGHWAY ENGINEERING-87	TRAFFIC ANALYSIS	PAVEMENT CONSTRUCTION MAINTENANCE & MANAGEMENT	HIGHWAY PROJECT FORMULATION AND ECONOMICS	PAVEMENT ANALYSIS AND DESIGN	ELECTIVE III LAND USE AND TRANSPORTATION MODELING TRANSPORTATION SYSTEM MANAGEMENT ROAD SAFETY ENGINEERING	ELECTIVE IV GIS APPLICATIONS IN TRANSPORTATION ENGINEERING OPTIMIZATION TECHNIQUES ENVIRONMENTAL IMPACT ASSESSMENT FOR TRANSPORTATION PROJECTS		

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Electronics and Communication Engineering-70	Coding Theory and Techniques	Digital Signal Processors and Architectures	Wireless Communications and Networks	(Elective –III) Speech Signal Processing	(OE2) Image and Video Processing	(Elective –IV) Wireless MIMO Communications	(Elective –IV) Network Security And Cryptography	
				Biomedical Signal Processing	Software Defined Radio	Optical Communications Technology		
				Radar Signal Processing	Adhoc Wireless Networks			
Highway Engineering -87	Pavement Construction, Maintenance & Management	Pavement Analysis and Design	Traffic Analysis	(Elective –III) Highway Project Formulation and Economics	(OE2) Optimization Techniques	(Elective –IV) Intelligent transportation systems		
				Land use and Transportation Modeling	Disaster Management	Rural Roads		
				Environmental Impact Assessment for Transportation Projects	Finite Element Method	Airport Engineering		

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